**St. Machar Academy**

**Homework 2**

**Logic Gates**

Complete the truth table for the following gates

The Inverter

|  |  |
| --- | --- |
| Input A | **Output Z** |
| 0 |  |
| 1 |  |

Input A

Output Z

The AND gate

|  |  |  |
| --- | --- | --- |
| **A** | **B** | **Z** |
| 0011 | 0101 |   |

A

B

Z

The OR gate

A

B



|  |  |  |
| --- | --- | --- |
| **A** | **B** | **Z** |
| 0011 | 0101 |   |

Z

The NAND gate

|  |  |  |
| --- | --- | --- |
| **A** | **B** | **Z** |
| 0011 | 0101 |   |



A

B

Z

|  |  |  |
| --- | --- | --- |
| **A** | **B** | **Z** |
| 0011 | 0101 |   |

The NOR gate

Z

A

B

Z